

Performance Characteristics:

- Frequency band: 6~18GHz
- Insertion loss: 1 dB/dB/dB / 0.6 0.6 1.1 1.1 dB/dB / 1.1 dB
- All measurements: 2dB/3dB/4dB/5dB/6dB/7dB
- Input/output voltage standing wave ratio: 1.2/1.2
- Chip size: 0.6mm×0.6mm×0.1mm

Product Description:

CW-AE0618-2, CW-AE0618-3, CW-AE0618-4, CW-AE0618-5, CW-AE0618-6, CW-AE0618-7 are GaAs MMIC series equalizer chips with frequency range covering 6GHz-18GHz. The insertion loss in the frequency range is a positive slope, and all measurements are 2dB, 3dB, 4dB, 5dB, 6dB and 7dB respectively.

Electrical parameters: ($T_A=25^{\circ}\text{C}$)

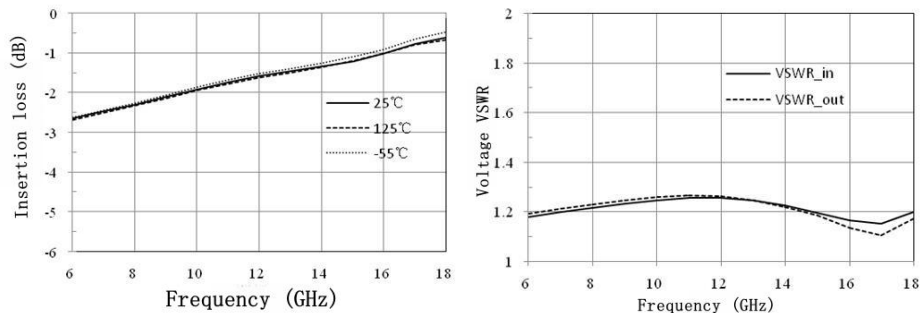
Indicators		Minimum	Typical value	Maximum	Units
Frequency range		6 ~ 18			GHz
Insertion loss	CW-AE0618-2	0.6	-	2.6	dB
	CW-AE0618-3	0.6	-	3.5	dB
	CW-AE0618-4	1	-	5.0	dB
	CW-AE0618-5	1.1	-	5.9	dB
	CW-AE0618-6	1.1	-	7	dB
	CW-AE0618-7	1.1	-	8	dB
Even measure	CW-AE0618-2	-	2	-	dB
	CW-AE0618-3	-	3	-	dB
	CW-AE0618-4	-	4	-	dB
	CW-AE0618-5	-	5	-	dB
	CW-AE0618-6	-	6	-	dB
	CW-AE0618-7	-	7	-	dB
Input voltage standing wave ratio		-	1.2	1.3	-
Output voltage standing wave ratio		-	1.2	1.3	-

Use limit parameters: (Exceeding any of the above maximum limits risks permanent damage.)

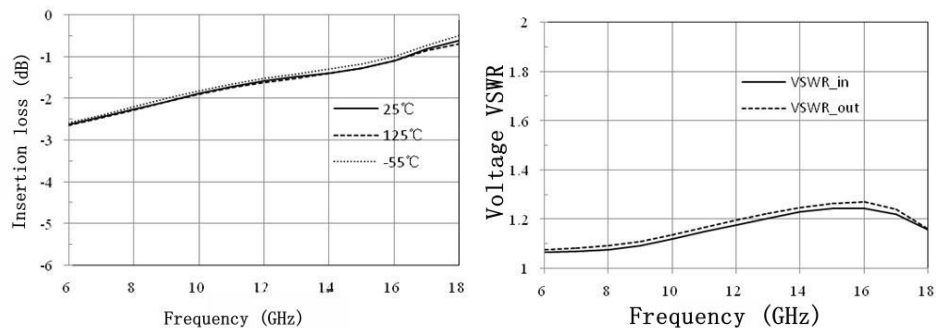
Input power	+30dBm
Storage temperature	-65°C~150°C
Service temperature	-55°C~125°C

Typical curve:

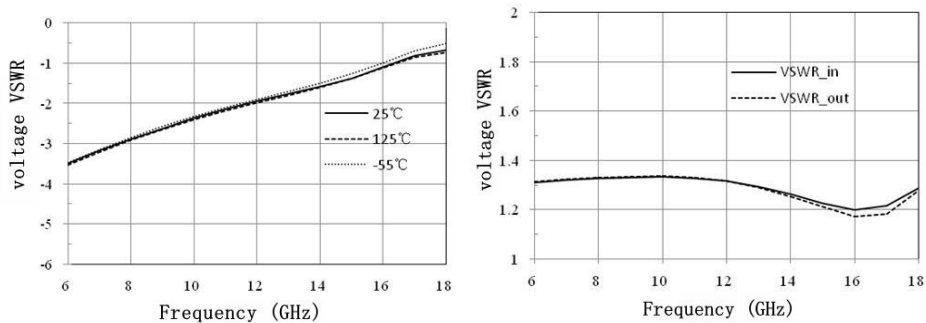
CW-AE0618-2 (on wafer)



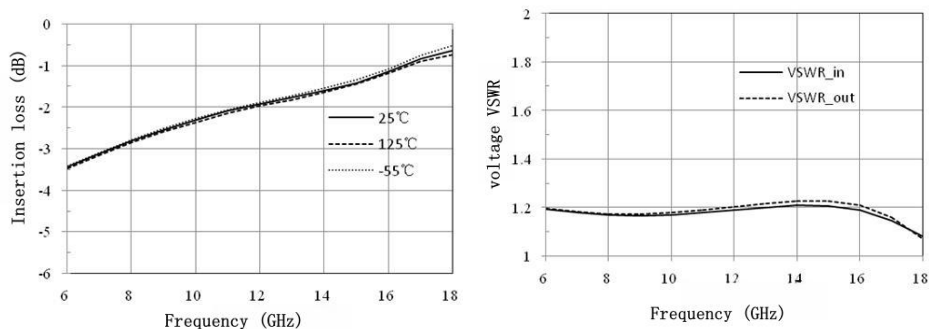
CW-AE0618-2 (bondwire)



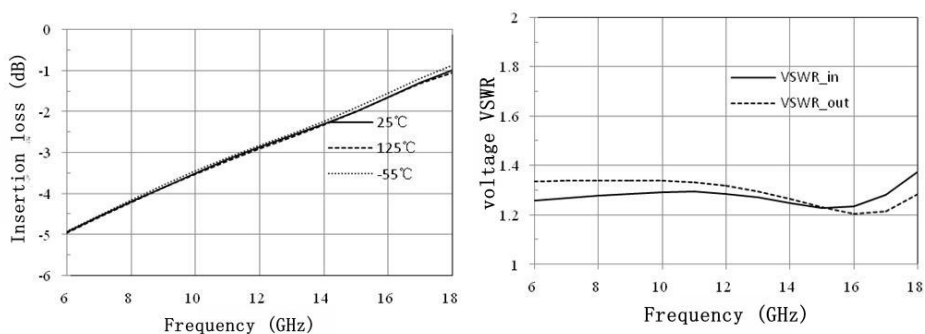
CW-AE0618-3 (on wafer)



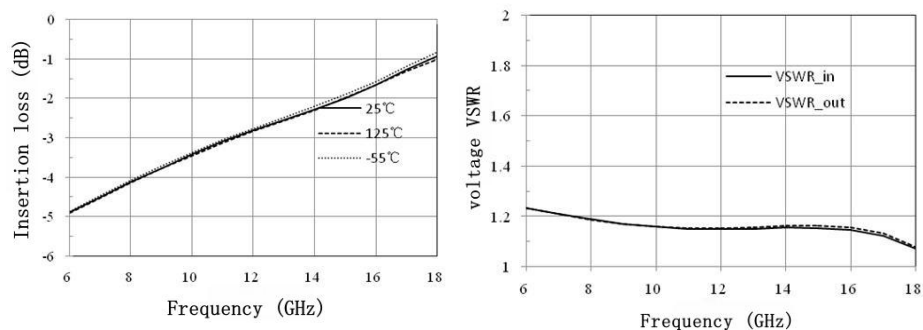
CW-AE0618-3 (bondwire)



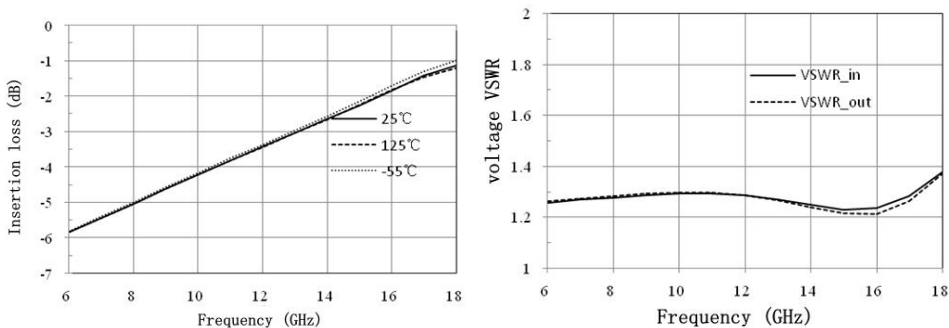
CW-AE0618-4 (on wafer)



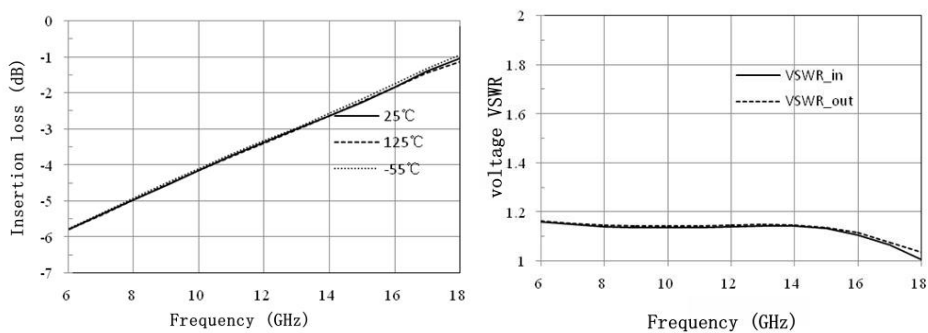
CW-AE0618-4 (bondwire)



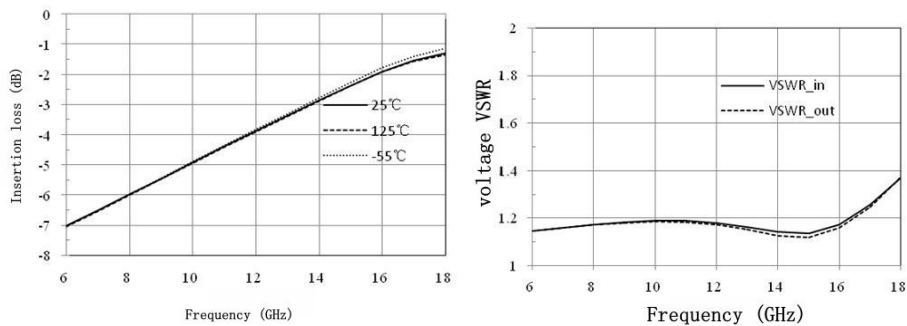
CW-AE0618-5 (on wafer)



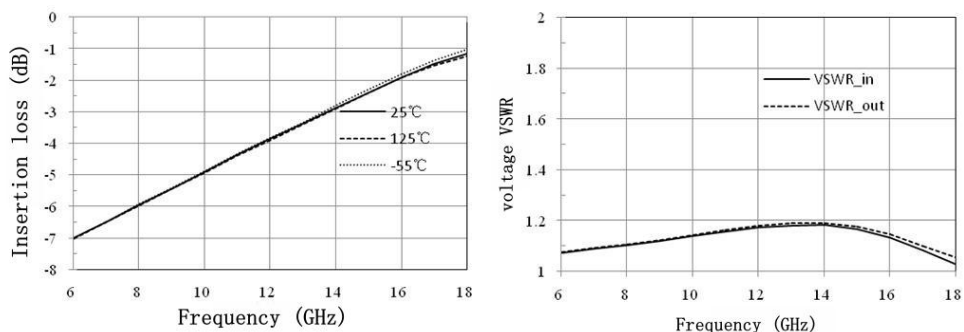
CW-AE0618-5 (bondwire)



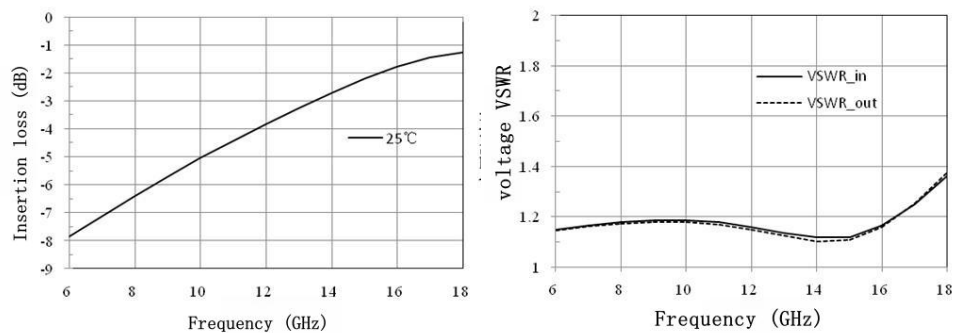
CW-AE0618-6 (on wafer)



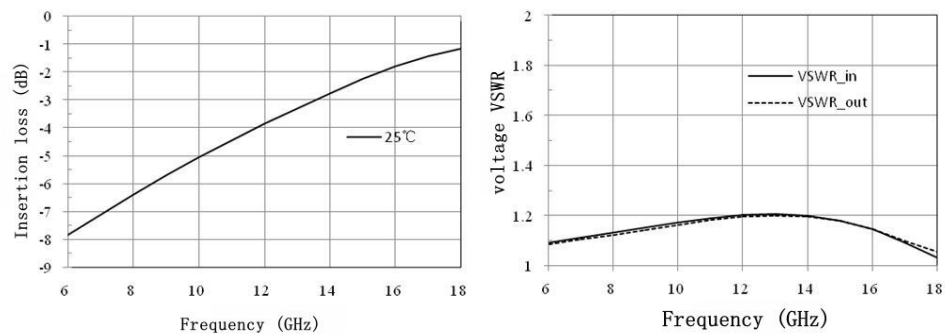
CW-AE0618-6 (bondwire)



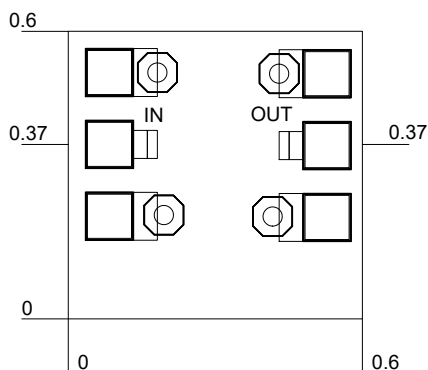
CW-AE0618-7 (on wafer)



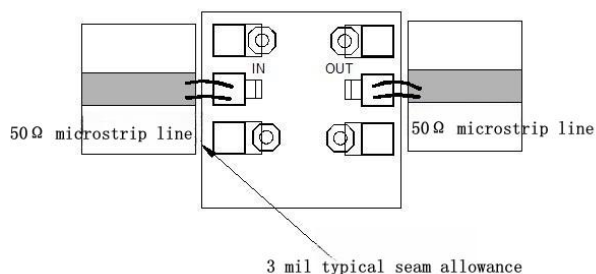
CW-AE0618-7 (bondwire)



Size drawing: (unit mm)



Suggested assembly drawing:



Instructions:

Storage: The chip must be placed in a container with electrostatic protection and stored in a nitrogen environment.

Cleaning treatment: The bare chip must be operated and used in a purified environment. It is forbidden to use liquid cleaning agent to clean the chip.

Electrostatic protection: Strictly comply with the ESD protection requirements to avoid electrostatic damage to the components.

General operation: Use vacuum chuck or precision pointed tweezers to pick up the chip. Avoid touching the surface of the chip with tools or fingers during handling.

Mounting operation: The chip can be installed using AuSn solder eutectic welding or conductive adhesive bonding process. The mounting surface must be clean and flat.

Bonding operation: Input and output with 2 (recommended diameter of 25um gold wire) bonding wire, bonding wire length less than 250um is optimal. It is recommended to use the smallest possible ultrasonic energy. Bonding begins at the pressure point on the chip and ends at the package (or substrate).