

**Performance Features:**

- Frequency range: 2-18GHz
- Insertion loss: 0.7Db
- Isolation: 20dB
- Input/Output: 50 Ohm match
- Chip size: 2.00mm x 2.00mm x 0.1mm

**Product Description:**

CW-PD0218S is a GaAs MMIC 2-way 0 degree power divider with excellent performance. The chip covers the 2-18GHz band range.

**Electrical parameters:** ( $T_A=25^{\circ}\text{C}$ )

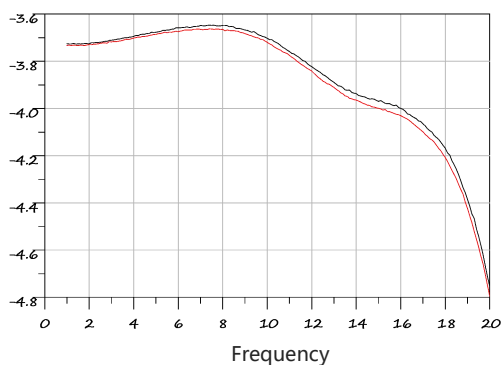
Indicators	Minimum	Typical value	Maximum value	Units
Frequency range	2-18			GHz
Insertion loss	-	0.7	-	dB
Flatness	-	$\pm 0.3$	-	dB
isolation	-	20	-	dB
Return loss	-	15	-	dB

**Use limit parameters:** (Exceeding any of the above maximum limits risks permanent damage.)

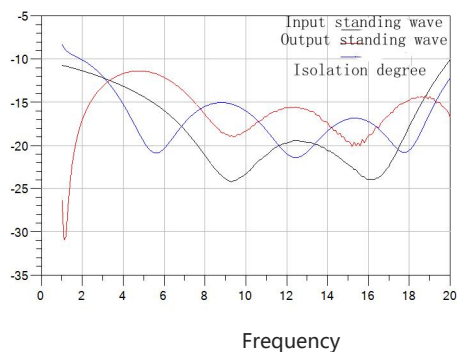
Maximum input power	37 dBm
Storage temperature	$-65^{\circ}\text{C}$ - $175^{\circ}\text{C}$
Service temperature	$-55^{\circ}\text{C}$ - $125^{\circ}\text{C}$

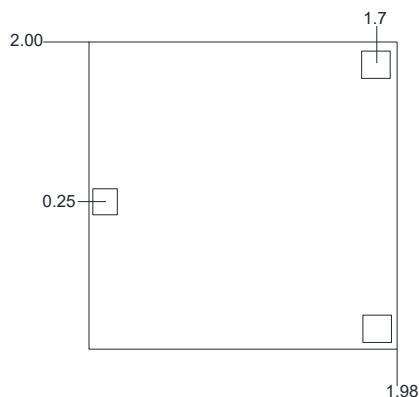
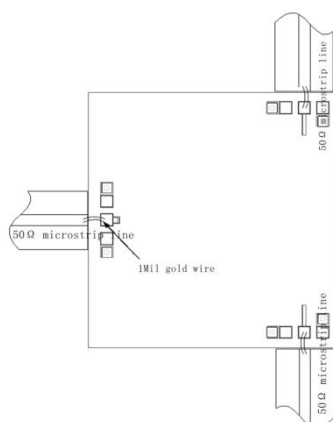
**Typical curve:** ( $T_A=+25^{\circ}\text{C}$ )

I/O return loss



Insertion loss



**Size drawing:** (unit mm)**Suggested assembly drawing:****Instructions:**

**Storage:** The chip must be placed in a container with electrostatic protection and stored in a nitrogen environment.

**Cleaning treatment:** The bare chip must be operated and used in a purified environment. It is forbidden to use liquid cleaning agent to clean the chip.

**Electrostatic protection:** Strictly comply with the ESD protection requirements to avoid electrostatic damage to the components.

**General operation:** Use vacuum chuck or precision pointed tweezers to pick up the chip. Avoid touching the surface of the chip with tools or fingers during handling.

**Mounting operation:** The chip can be installed using AuSn solder eutectic welding or conductive adhesive bonding process. The mounting surface must be clean and flat.

**Bonding operation:** Input and output with 2 (recommended diameter of 25μm gold wire) bonding wire, bonding wire length less than 250μm is optimal. It is recommended to use the smallest possible ultrasonic energy. Bonding begins at the pressure point on the chip and ends at the package (or substrate).